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	Filing Date	April 24, 2001
	First Named Inventor	Steven A. Guccione
	Examiner Name	Pranav Chandrasekhar
	Art Unit	2115
	Patent No.	
	Attorney Docket Number	X-806 US

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X-806 US
09/841,270

PATENT
Conf. No.: 6015

IN THE UNITED STATES PATENT OFFICE

Applicants: Steven A. Guccione et al.
Assignee: Xilinx, Inc.
Title: "AUTOMATIC TRACKING AND ASSEMBLY OF CHANGED
PORTIONS OF CONFIGURATION DATA FOR PARTIAL RUN-
TIME RECONFIGURATION OF A PROGRAMMABLE LOGIC
DEVICE"

Ser. No.: 09/841,270 File Date: 04/24/01
Examiner: Pranav Chandrasekhar Art Unit: 2115
Docket No.: X-806 US Conf. No.: 6015

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AMENDMENT AND RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed April 7, 2004,
Applicants submit the following amendments and remarks.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 8 of this paper.